

**ON-CHIP 2D ADJUSTABLE DEFECTIVE PIXEL FILTERING
FOR CMOS IMAGERS**

ABSTRACT

5 The invention provides for the filtering of pixel defects from CMOS imagers. The invention comprises scanning each of a plurality of pixels within a block (420), and designating a pixel as a process pixel (430). The process pixel is then compared to adjacent pixels to see if the process pixel value deviates significantly from an adjacent pixel value (450), to determine if error correction is needed.

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